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By: Moritz Wolff

Date: Jan 11, 2002

2823  
FEB 11 2002  
CIPR  
Patent & Trademark Office  
2823

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Thomas Huttner et al.

Applic. No. : 09/313,424

Filed : May 17, 1999

Title : SOI Semiconductor Configuration And Method Of  
Fabricating The Same

Examiner : Brook Kebede

Group Art Unit : 2823

AMENDMENT

Hon. Commissioner of Patents and Trademarks,  
Washington, D. C. 20231

Sir :

Responsive to the non-final Office action dated October 18, 2001, kindly amend the above-identified application as follows:

In the Specification:

Replace the paragraphs on page 12, lines 1-12, with --

Figs. 3a to 3f are diagrammatic vertical sectional views of a sequence with a third design variant of the invention, in

TC 2823 MAIL ROOM  
FEB 11 2002  
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